

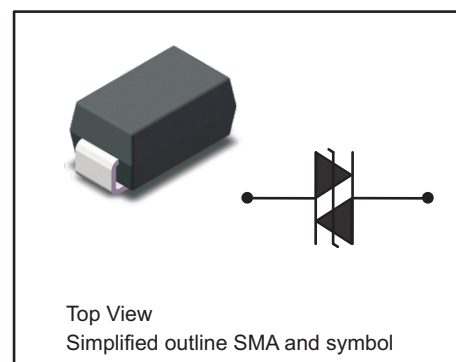
1-Line, Bi-directional, Thyristor Surge Suppressors

DESCRIPTION

TSS thyristors are a type of semiconduct component. They are designed for transient surge protection.

FEATURES

- Excellent capability of absorbing transient surge
- Quick response to surge voltage (ns Level)
- Eliminates overvoltage caused by fast rising transients
- Does not degrade surge capability after multiple surge within limit
- Fails short circuit when surged in excess of ratings
- Low leakage current



MECHANICAL DATA

- Case: SMA
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.055g / 0.002oz

ABSOLUTE MAXIMUM RATINGS(TA=25°C,RH=45%-75%,unless otherwise noted)

Parameter	Symbol	Value	Unit
Storage temperature range	T_{stg}	-55 to+150	°C
Operating junction temperature range	T_j	-55 to+125	°C
Repetitive peak pulse current (10/1000us)	I_{PP}	45	A

ELECTRICAL CHARACTERISTICS(Ta=25°C,continued)

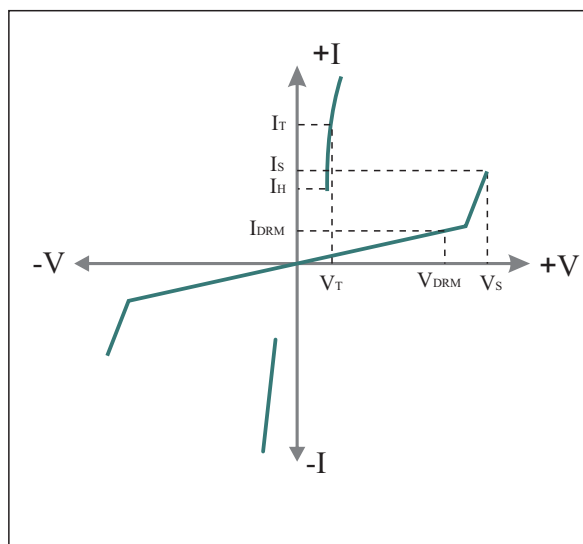
Part Number	V_{DRM}	I_{DRM}	V_s	I_s	I_H	V_T	I_T	$C_o^{(1)}$
	V	uA	V	mA	mA	V	A	pF
	Min.	Max.	Max.	Max.	Min.~Max.	Max.	Max.	Typ.
P0080TALN	6	1	12	800	20~100	3	2.2	50

(1) Off-state capacitance is measured in $V_{DC}=2V, f=1MHz$



ELECTRICAL CHARACTERISTICS (TA=25°C)

Symbol	Parameter
V_{DRM}	Peak off-state voltage
I_{DRM}	Peak off-state current
V_S	Switching voltage
I_S	Switching current
V_T	On-state voltage
I_T	On-state current
I_H	Holding current
C_O	Off-state capacitance
V_{BR}	Reverse breakdown voltage
I_{BR}	Test current

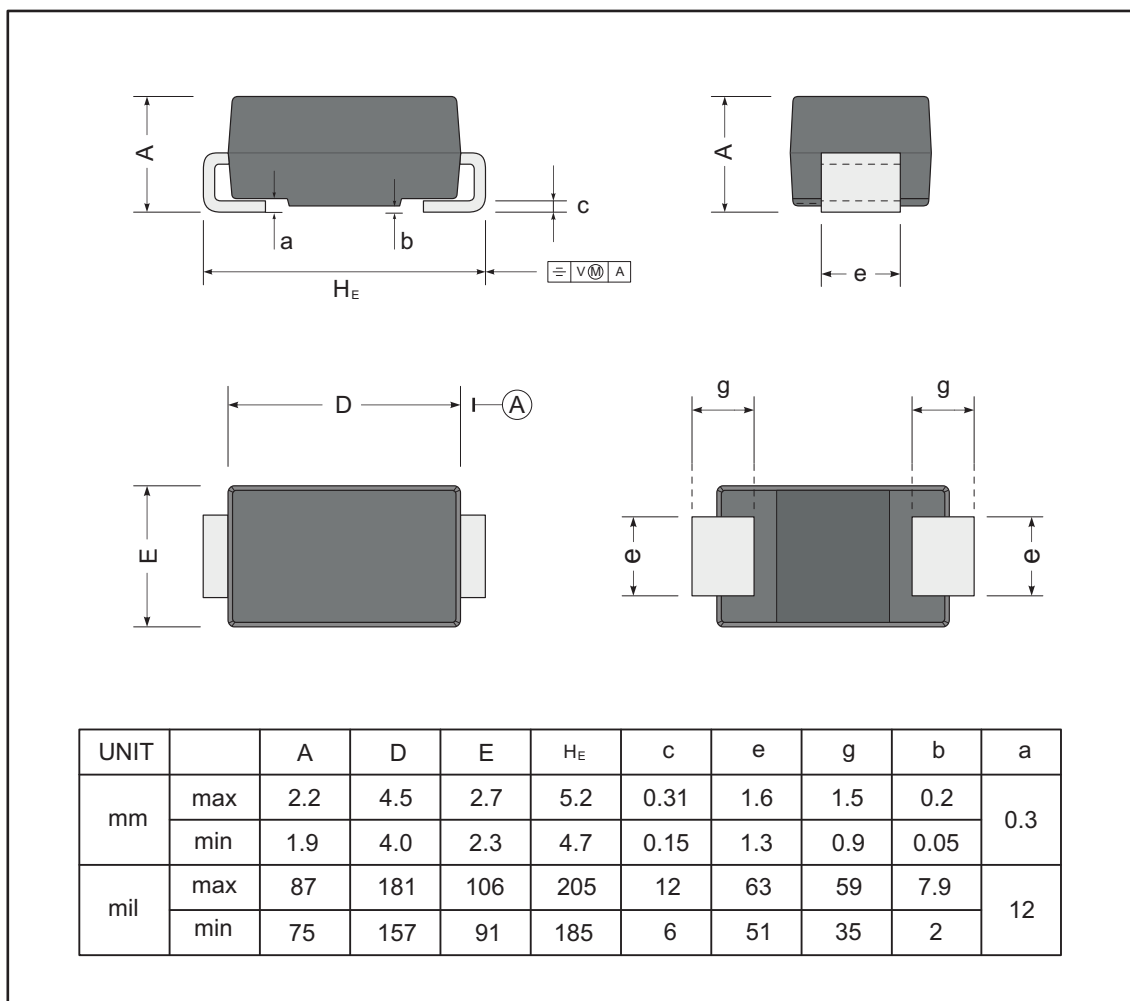




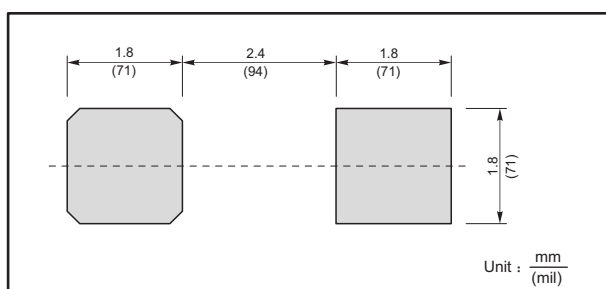
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SMA



The recommended mounting pad size



Marking

Type number	Marking code
P0080TALN	P-8AN



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